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PERFORMANCE ANALYSIS OF 2D CONVERTER BY COMBINING SR & KY CONVERTERS

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Abstract
Most of the portable equipments use battery as power source. The increasing use of low voltage portable devices and growing requirements of functionalities embedded into such devices. Thus an efficient power management technique is needed for longer battery life for them. Highly variable nature of batteries systems often require supply voltages to be both higher and lower than the battery. This is most efficiently generated by a buck-boost switching converter. But here the converter efficiency is decreased since the power loss occurs in the storage devices. Step by step, process of designing, feedback control and simulation of a novel voltage-buck boost converter, combining KY and synchronous Rectifier buck converter for battery power applications. Unlike the traditional buck–boost converter, this converter has the positive output voltage and system is stable, different from the negative output voltage and low stable of the traditional inverting buck–boost converters. Since such a converter operates in continuous conduction mode. Also it possesses the non-pulsating output current, thereby not only decreasing the current stress on the output capacitor but also reducing the output voltage ripple. Both the KY converter and the synchronous buck converter, combined into a positive buck–boost converter, uses the same power switches. Here it makes the circuit to be compact and the corresponding cost to be down. Voltage conversion ratio is 2D,so it is also called 2D converter.
Index Terms- buck-boost converter, PI control, KY converter, synchronously rectified (SR) buck converter

I. INTRODUCTION

Over the years the portable electronics industry progressed widely. A lot requirement evolved such as increased battery life, small and cheap systems, coloured displays and a demand for increased talk-time in mobile phones. The increasing demand from power systems has placed power consumption at a peak. To keep up with these demands an engineer has worked towards developing efficient conversion techniques and also has resulted in the growth of an interdisciplinary field of Power Electronics. However the introduction of new field has offered challenges owing to the unique combination of three major fields of electrical engineering: electronics, power and control. DC-DC converters are the devices that are used to convert and control the DC electrical power efficiently and effectively from one voltage level to another. The DC-DC converter is a device for converting one DC voltage level to another DC voltage level with a minimal loss of energy. DC conversion technique having a great importance in many applications, mainly from low to high power applications. The circuit mainly consists of at least two semiconductor switches and one energy storage element. The semiconductor switches combines with a diode and a transistor/MOSFET. The Filters made of capacitors. They are normally added to the output of the converter to reduce output voltage ripple.

A few applications of DC-DC converters are 5V DC on a personal computer motherboard must be stepped down to 2.5V, 2V or less for one of the latest CPU chips. Where 2V from a single cell must be stepped up to 5V or more, in electronic circuitry operation. Also in LED TV, protection, metering devices will need 12V output voltage. By introduce DC-DC converter to step up or step down the voltage to the system. In all of these applications, to change the DC energy from one voltage level to another, while wasting of energy as little as possible in the process.

In other words, to perform the conversion effectively with the highest possible efficiency. DC-DC Converters are in hit list because unlike AC, DC can't simply be stepped up or down using a transformer. DC-DC converter is the DC equivalent of a transformer in many ways. They are essentially just change the input energy into a different level. So what ever the output voltage level, the output power all comes from the input. The fact that some are used up by the converter circuitry and components, in doing their work efficiently. A Positive Buck-Boost converter is a DC-DC converter which is controlled to act as Buck or Boost mode with same polarity of the input voltage. This converter has four switching states which include all the switching states of the common DC-DC converters. In addition there is one switching state which provides a degree of freedom for the positive Buck-Boost converter in comparison to the Buck, Boost, and inverting Buck-Boost converters. In other words the Positive Buck-Boost Converter shows a higher level of flexibility, because its inductor current can be controlled compared with the other DC-DC converters.

The most common power management problem, especially for battery powered electronics applications, is the need to provide a regulated output voltage from a battery voltage which, when it is charged or discharged. It can be greater than, less than, or equal to the desired output voltage. There are several existing solutions to this problem. But all have significant drawbacks. They are: cascaded buck-boost converter; linear regulator; SEPIC converter; classic 4-switch buck-boost converter; and Cuk-converter. The proposed solution has advantages over all of these converters. Mainly they can improve the efficiency and the simplification of the circuitry needed.

A KY buck–boost converter has been introduced to conquer the mentioned drawbacks of the system. A common buck converter with KY boost converter, it has a serious problem in four power switches used. It causes the corresponding cost to be high. Also the switching losses are increased due the increase in number of switching devices. In order to reduce the number of power switches, the KY converter and the SR buck converter, combined into a buck–boost converter. It also called 2D converter because of voltage conversion ratio is 2D. Also the proposed converter has left half of plane poles away from imaginary axis, so system is stable. It is having fast transient response due to the input connected to the output during the turn-on period. This converter always operates in continuous current conduction mode due to the positive and negative inductor currents existing at light load. As compared with the other converters, this converter has the non-pulsating output inductor current, thereby causing the current stress on the output capacitor to be decreased. Also the corresponding output voltage ripples to be less. Moreover, this non -inverting converter has the positive output voltage different from the negative output voltage of the traditional buck–boost converter.

In this paper, the detailed study and operation of this converter, along with some experimental results provided to verify the application wise performance analysis.

II. CIRCUIT CONFIGURATIONS

Normally many applications require voltage-bucking/boosting converters such as LED TV, mobile devices, portable devices, protection devices, metering devices, car electronic devices, etc. This is because the battery has quite large variations in output voltage; and hence the additional switching power device is needed for processing the varied input voltage so as to generate the stabilized output voltage. There are several types of non-isolated voltage buck-boost converter, such as Cuk converter, Zeta converter, single-ended primary-inductor converter (SEPIC), Luo converter and its derivatives, etc. However these converters are operating in the continuous conduction mode (CCM). But while taking transfer function to analyse that they possess left half of the plane poles which are near to imaginary axis, thus causing system stability to be low by root locus. root locus is a method to establish the stability of a single input, single output system. For the application of the power supply using the low voltage battery, analogue circuits, such as radio frequency amplifier often need high voltage to obtain enough output power and voltage amplitude. This is done by boosting the low voltage to the high voltage required. Therefore, for many of computer, mobile electronic products to be considered, there are some converters needed to supply above or under voltage especially for portable communications systems, such as iPods, musical devices, Bluetooth devices, personal digital iPods, etc. For such applications, the output voltage ripple must be taken into account. Regarding the traditional inverting buck–boost converter, their output currents are pulsating, thereby, causing the corresponding output voltage ripples to be large. To overcome these problems, one way is to use the capacitor with large capacitance and low equivalent series resistance (ESR) and another way is to add an LC filter to reduce ripples. Also can increase the switching frequency to reduce the mentioned drawbacks.

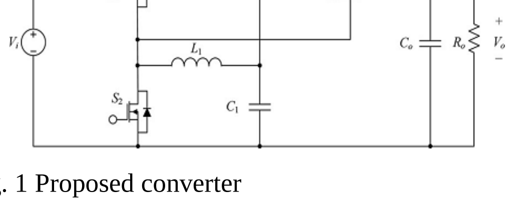

Fig. 1 Proposed converter

Figure:1 shows a novel buck–boost converter, which combines a synchronous buck converter and KY boost converter. The SR buck converter, which consists of two power switches S1 and S_2, one inductor L_1, one energy -transferring capacitor C_1. The other KY converter is constructed by two power switches S_1 and S_2, one power diode D_1, one energy-transferring capacitor C_2, one output inductor L_2 and one output capacitor Co. The output load is a resistive load and is signified by Ro.

III. OPERATING PRINCIPLE

The proposed system structure is derived from conventional positive buck boost converter (fig. 1).$S_1$and S_2 are the main switches. All the components are assumed ideal. The values of C_1 and C_2 Are large enough to keep Vc_1 and Vc_2 almost constant. Thus the variations in $Vc_1$and Vc_1 are small during the charging and discharging period. The dc input voltage is represented by Vi, the dc output voltage is represented by Vo, the dc output current is denoted by io. The gate driving pulses for S_1 and S_2 are indicated by M_1 and M_2. The voltages on S_1 and S_2 are represented by Vs_1 and Vs_2. The voltages on L_1 and L_2 are denoted by V_{L1} and V_{L2}. The currents in L_1 and L_2 are signified by I_{L1} and I_{L2}. The currents flowing through L_1 and L_2 are both positive. Since this converter always operates in CCM, thus the turn-on type is (D, 1−D), where D is the duty cycle.

During the magnetization period, the input voltage of the KY converter comes from the input voltage source, whereas during the demagnetization period, the input voltage of the KY converter comes from the output voltage of the SR buck converter. In addition, during mode1 operation switches S1 being ON and S_2 being OFF, L_1 and L_2 are both magnetized. At the same time,C_1 is charged, and hence, the voltage across C1 is positive, whereas C_2 is reversely charged; and hence, the voltage across C_2 is negative. During the mode 2 operation switches S1being OFF and S_2 being ON, L_1 and L_2 are both demagnetized. At the same time, C_1 is discharged and C_2 is reverse charged with the voltage across C_2 being from negative to positive. Finally, the voltage across C_2 is the same as the voltage across C_1. Thus the working cycle continues as per sequences.

A. Mode 1

As shown in figure: 3, S1 is turned OFF but S2 is turned ON. During this state, the energy stored in L1 and C1 is released to C2 and the output via L2. The voltage across L1 is minus VC1, thereby causing L1 to be demagnetized and C1 .is discharged. At the same time, the voltage across L2 is VC2 minus Vo,

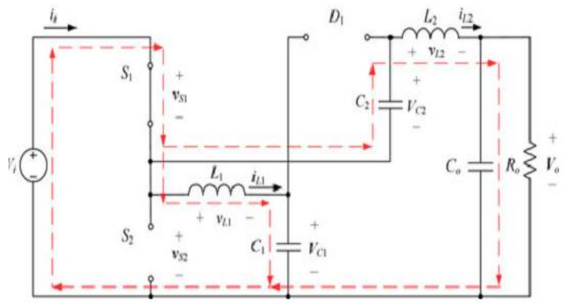

Fig. 2. Current flow mode 1

there by causing L2 to be demagnetized, and C2 is charged. Therefore, the working mode equations are represented as follows

$$V_{L1}=V_i-V_{c1} \quad (1)$$
$$V_{L2}=V_i+V_{c2}-V_o \quad (2)$$

B. Mode 2

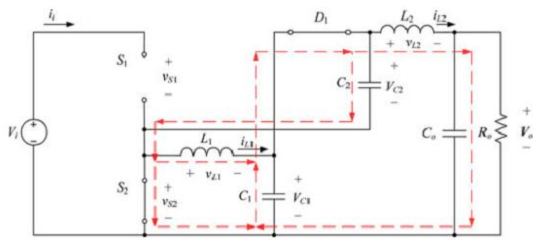

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Fig. 3. Current flow mode 2

As shown in figure: 3, S1 is turned OFF but S2 is turned ON. During this state, the energy stored in L1 and C1 is released to C2 and the output via L2. The voltage across L1 is minus VC1, thereby causing L1 to be demagnetized and C1 .is discharged. At the same time, the voltage across L2 is VC2 minus Vo, thereby causing L2 to be demagnetized, and C2 is charged. Therefore, the working mode equations are described as follows:

$$V_{L1} = -V_{C1} \quad (3)$$
$$V_{L2} = V_{C2} - V_o \quad (4)$$
$$V_{C2} = V_{C1} \quad (5)$$

By applying the voltage-second balance to (1) and (3), the following equation can be obtained as

$$(V_i-V_{c1})DT_s+(-V_{c1})(1-D)T_s=0 \quad (6)$$
$$V_iDT_s-V_{c1}DT_s-V_{c1}T_s+V_{c1}DT_s=0$$
$$V_iDT_s=V_{c1}T_s$$

Therefore, by simplifying (6), the following equation can beobtained as

$$V_{c1}=DV_i \quad (7)$$

Sequentially, by applying the voltage-second balance to (2) and (4), the following equation can be obtained as

$(V_i+V_{c2}-V_o)DT_s+(V_{c2}-V_o)(1-D)T_s=0$ (8)

Hence, by substituting (5) and (7) into (8), the voltage con-version ratio of the proposed converter can be obtained as

$V_iDT_s+V_{c2}DT_s-V_oDT_s+V_{c2}T_s-V_{c2}DT_s-V_oT_s+V_oDT_s=0$

$V_iDT_s+V_{c2}T_s-V_oT_s=0$ (9)

Substitute 5 in 9

$V_iDT_s+V_{c1}T_s-V_oT_s=0$ (10)

Substitute 7 in 10

$V_iDT_s+V_iDT_s-V_oT_s=0$

$2DV_iT_s-V_oT_s=0$

$2DV_iT_s=V_oT_s$

The voltage conversion ratio of the proposed converter can be obtained as

$$\mathbf{Vo/Vi=2D} \quad \mathbf{(11)}$$

Therefore, such a converter can operate in the buck mode as the duty cycle D is smaller than 0.5, whereas it can operate in the boost mode as D is larger than 0.5.
In addition, based on (5), (7), and (9), the dc voltages across $C_1$and C_2 can be expressed to be

Hence $V_{c1}=V_{c2}=0.5Vo$ (12)

IV. APPLICATION IN BATTERY CHARGER

A common power management problem, especially for battery powered electronics applications, is the need to provide a regulated output voltage from a battery voltage, when charged or discharged. They can be greater than, less than, or equal to the desired output voltage. There are several existing solutions to this problem; But each having significant drawbacks. However, new technologies has developed a solution for a buck-boost converter which maximizes efficiency, minimizes ripple noise on input and output, and minimizes external component requirements and associated cost. Also achieve efficient output voltage effectively. The modified non-inverting buck-boost converter in a combination of different modes as required by the application.The DC-DC converter uses a combination of buck-boost converter, boost converter mode to charge the Li-ion battery as an example. In case of Li-ion, the constant current constant voltage (CC CV) charging is used to charge the battery. Here the input voltage just enough to show the functionality of the converter in buck-boost mode.

V. CONTROLLER DESIGN

Based on the research done on the battery power application it is now possible to start developing an efficient control for the plant. As our task is to control the duty cycles of each pair of switches and their phase, so as to ensure the following conditions. Reaching the target steady state should happen in the desired manner, i.e. the controller needs to handle transients properly.

The controller also needs to be able to reject disturbances. First and most important is the reaching and stabilizing around a given output voltage demand. While doing all this, the controller needs to choose among the infinite possibilities of inputs that would satisfy the above conditions, that will cause the least losses. To overcome the limitations of the open -loop controller, control theory introduces feedback. A closed-loop controller uses feedback to control outputs of the system. Closed-loop controllers have the following advantages over open -loop controllers:

1. Disturbance rejection.
2. Guaranteed performance even.
3. Unstable processes can be stabilized.
4. Reduced sensitivity to parameter variations.
5. Improved reference tracking performance.

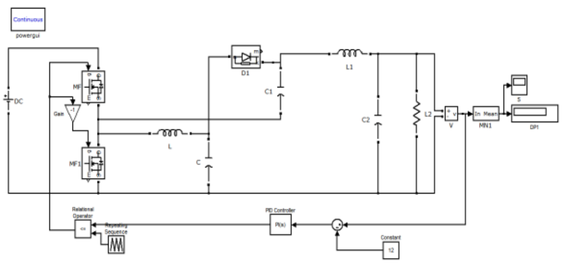

Fig. 5. Simulation block diagram

In this paper PI controller is used for feedback control. PI controller will eliminate forced oscillations and steady state error resulting in operation of on-off controller and P controller respectively. However, introducing integral mode has a negative effect on speed of the response and overall stability of the system.It can be expected since PI controller does not have means to predict what will happen with the error in near future. This problem can be solved by introducing derivative mode which has ability to predict what will happen with the error in near future and thus to decrease a reaction time of the controller. PI controllers are very often used in industry. A control without D mode is used when; Fast response of the system is not required and large transport delay in the system. Also large disturbances and noise are present during operation of the process.

Since the controller parameter tuning method is widely used in the industry, there are three steps to online tune the parameters of the voltage controller to be described in the following. The proportional gain kp is tuned from zero to the value which makes the output voltage very close to about 80% of the prescribed output voltage. After this, the integral gain ki is tuned from zero to the value which makes the output voltage very close to the prescribed output voltage but somewhat oscillate. Then, ki will be reduced to some value without oscillation.

VI. KEY DESIGN PARAMETER CONSIDERATIONS

In this section, the design of inductors and capacitors are mainly taken into account. Before this section is taken up, there are some specifications to be given as follows: 1) the dc input voltage Vi is from 10 V to 16 V; 2) the dc output voltage Vo is 12 V; 3) the rated dc load current Io−rated is 3 A; 4) the switching Frequency fs is 200 kHz; and 5) the product name of S1 and S2 is APM3109 and the product name of D1 is STP30L45 and 6) the product name of the control IC is ICA7W716.

Inductor Design

From an industrial point of view, the inductor is designed under the condition that no negative current in the inductor exists above 25% of the rated dc load current. Therefore, in this letter, the critical point between positive current and negative current in the inductor is assumed at 25% of the rated dc load current. Therefore, the peak-to-peak values of iL1 and iL2 are expressed byΔiL1andΔiL2, respectively, and can be obtained according to the following equation:

$$\Delta iL1 = \Delta iL2 = 0.5Io{-}rated.$$

Therefore,ΔiL1andΔiL2are 1.5 A.
Since the high input voltage makes the inductor not easier to escape from the negative current than the low input voltage, the inductor design is mainly determined by the high input voltage, namely, 16 V. Hence, the corresponding minimum duty cycle Dmin is 0.375. Moreover, based on (10) ,V_{C1} and V_{C2} are both 0.5Vo, namely, 6 V. Also, the values of L_1 and L_2 can be obtained according to the following equations:

$$L_1 \geq D_{min} (V_i - V_{c1})/\Delta i_{L1} * f_s \quad (13)$$
$$L_1 \geq 0.375(16-0.5*12)/1.5*200kHz$$
$$L_1 \geq 12.5Mh$$
$$L_2 \geq D_{min} (V_i + V_{c2} - V_o)/\Delta i_{L2} * f_s \quad (14)$$
$$L_2 \geq 0.375(16+6-12)/1.5*200kHz$$
$$L2 \geq 12.5Mh$$

Therefore, the values of L_1 and L_2 both are calculated to be not less than 12.5µH

Capacitor Design

Energy-Transferring Capacitor Design: Prior to design-ing the energy-transferring capacitorsC_1 and C_2 ,it is assumed that the values of C_1 and C_2 are large enough to keep V_{C1} and V_{C2} almost at 6 V, and hence, variations in V_{C1} and V_{C2} are quite small and are defined to be $\Delta V_{C1}$and ΔV_{C2}, respectively. Based on this assumption, ΔV_{C1} and ΔV_{C2} are both set to smaller than 1% of $V_{C1}$and V_{C2}, respectively, namely, both are smaller than 60 mV. Also, in State 1, $C_1$is charged where as C_2 is discharged. Therefore, the values of C_1 andC_2 must satisfy the following equations:

$$C_1 \geq D_{max} * I_o\text{-rated}/ \Delta V_{c1} * f_s \quad (15)$$
$$C_1 \geq 0.625*3/6\ mV*200kHz$$
$$C_1 \geq 150\ \mu F$$
$$C_2 \geq D_{max} * Io\text{-rated}/ \Delta V_{c1} * f_s \quad (16)$$
$$C_2 \geq 0.625*3/6\ mV*200kHz$$
$$C_2 \geq 150\ \mu F$$

Since the maximum duty cycle Dmax occurs at the input voltage, both the values of C_1 andC_2 are not less than 150µF

VII. RESULTS AND DISCUSSION

The proposed 2D buck-boost converter in figure: 1, was simulated using a computer simulation program. Figure: 5 shows simulation diagram and waveforms of the converter for the closed loop system with resistive load. The following parameters were adopted in this simulation:

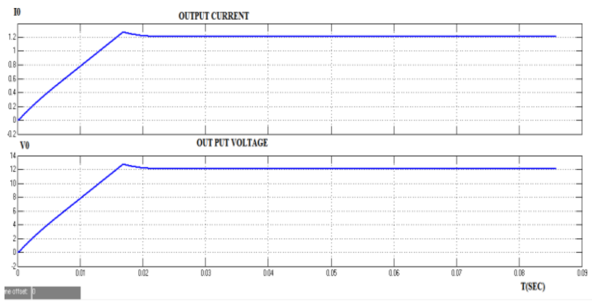

Fig. 7. Out put current & voltage waveforms

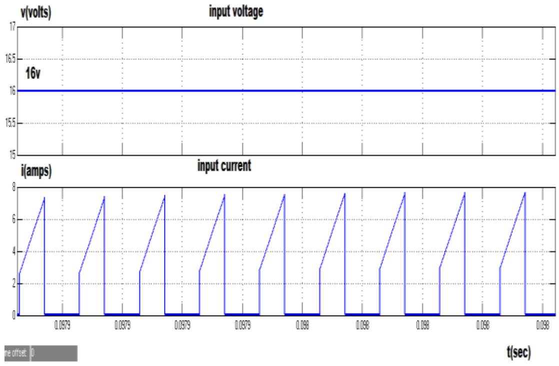

Fig. 8. Input voltage & current waveforms

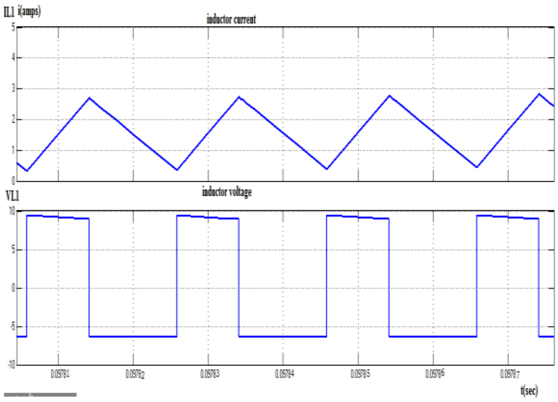

Fig. 9. Inductor 1 current & voltage waveforms

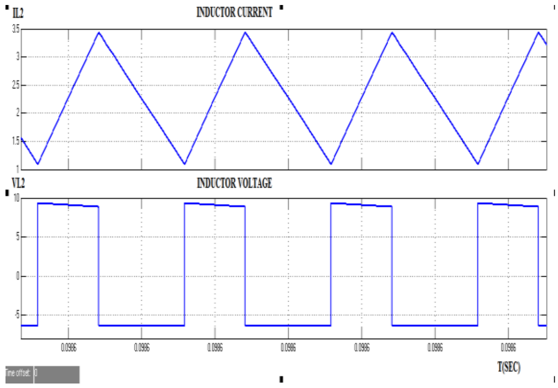

Fig. 10. Inductor 2 current & voltage waveform

From fig.7 It can observe that it have output current and output voltage waveform in which observed that 12 volts output and fast transient response of about 0.018sec. As fast transient response is due to which input is directly connected to output. current varies according to resistive load.

From fig 8 It can observe input voltage is 16v or it may be between 10 to 16 volts by controlled to 12v out put. input current contains one spike due to using of inductor. input current equals 2 times of output current.

From fig 9, 10 inductor current is non pulsating type in which current stress on output capacitor, voltage ripple at the output decreases. by current waveform can observe that converter operate in CCM.

The proposed converter has the voltage conversion ratio of 2D, and hence it possesses voltage bucking with the duty cycle locating between 0 and 0.5 and voltage boosting with the duty cycle locating between 0.5 and 1. By calculation with system working equation proposed converter voltage conversion ratio as 2D.Here an input of 10-16V dc supply is given. The converter works in a linear mode by giving a constant 12V output. Normally many applications working in a voltage range of 12V. Mobile phones and LED TV are working in a voltage range of 12V. Thus introduce this type of converter. By giving an input of 10V. Get a constant output of 12V by the voltage boosting action. Input of 16V get an output of constant 12V by voltage bucking action. By giving any voltage in between 10 to 16V, get a constant voltage of 12V.Here the voltage bucking/boosting action done by the converter with the feedback PI controller. Unlike the traditional buck–boost converter, proposed converter possesses fast transient responses. This converter is very suitable for low-ripple applications. As for the efficiency, this converter has the efficiency of 91%. Indeed, the proposed converter is suitable for the small-power applications because the surge current created by the charge pump is indispensable. But, using the soft switching with surge current suppressed can overcome this problem, and hence, makes this converter likely to be operated in high-power applications. The proposed converter is more efficient and effective than other positive buck-boost converter like Zeta converter, single-ended primary -inductor converter (SEPIC), Luo converter.

I. CONCLUSION

The proposed buck–boost converter, combining the KY converter and the traditional SR buck by using the same power switches, has a positive output voltage and left half of plane poles away from imaginary axis, so system is stable . Furthermore, this converter always operates in CCM inherently, there by causing variations in duty cycle all over the load range not to be so much, and hence, the control of the converter to be easy. Above all, such a converter possesses the non-pulsating inductor current, thereby not only decreasing the current stress on the output capacitor but also reducing the output voltage ripple. By means of experimental results, it can be seen that for any input voltage, the proposed converter can stably work for any dc load current; The Positive Buck Boost Converter Widely Used In Many Applications such as batter power. Thus a Micro system has developed a solution for a buck-boost converter which maximizes efficiency, minimizes ripple noise on input and output, and minimizes external component requirements and associated cost.

REFERENCES

[1] R. W. Erickson and D.Maksimovic, Fundamentals of Power Electronics, 2nd ed. Norwell, MA, Kluwer, 2001.
[2] N. Mohan, T. M. Undeland, and W. P. Robbins, Power Electronics, 2nd ed. New York: Willey, 2003.
[3] F. L. Luo, "Positive output Luo converters: Voltage lift technique," IEE Proc. Elect. Power Appl., vol. 4, no. 146, pp. 415–432, Jul. 1999.
[4] X. Chen, F. L. Luo, and H. Ye, "Modified positive output Luo converter," in Proc. IEEE Int. Conf. Power Electron. Drive Syst., 1999, pp. 450–455.
[5] F. L. Luo and H. Ye, "Positive output super-lift converters," IEEE Trans. Power Electron., vol. 18, no. 1, pp. 105–113, Jan. 2003.
[6] F. L. Luo and H. Ye, "Positive output multiple-lift push-pull switched-capacitor Luo-converters," IEEE Trans. Ind. Electron., vol. 51, no. 3, pp. 594–602, Jun. 2004.
[7] M. Zhu and F. L. Luo, "Development of voltage lift technique on double-output transformerless DC–DC converter," inProc. 33rd Annu Conf. Ind. Electron. Soc., 2007, pp. 1983–1988.
[8] M. Zhu and L. Luo, "Implementing of developed voltage lift technique on SEPIC, Cuk and double-output DC–DC converters," inProc. IEEE Int. Conf. Ind. Electron., 2007, pp. 674–681.
[9] K. Viswanathan, D. Srinivasan, and R. Oruganti, "A universal fuzzy con-troller for a non-linear power electronic converter," in Proc. IEEE Int. Conf. Fuzzy Syst., 2002, pp. 46–51.
[10] K. I. Hwu and Y. T. Yau, "Two types of KY buck–boost converters," IEEE Trans. Ind. Electron., vol. 56, no. 8, pp. 2970–2980, Aug. 2009.
[11] B. Axelrod, Y. Berkovich, and A. Ioinovici, "Hybrid switched-capacitor-Cuk/Zeta/Sepic converters in step-up mode,"Proc. IEEE Int. Symp. Cir-cuits Syst., pp. 1310–1313, 2005.

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